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INFORMATION DISCLOSURE CITATION  (Use several sheets if necessary)		APPLICANT Toshihiko OKAMURA					
		FILING DATE August 31, 2001	GROUP ART UNIT 2634 Unassigned				
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
DH	A1	6,252,917	6/26/01	Freeman	375	340	09/942,671 1050090908
<b>FOREIGN PATENT DOCUMENTS</b>							
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
							Y E S
DH	A2	2000-101453	4/7/00	Japan			Abstract
DH	A3	2000-201085	7/18/00	Japan			Abstract
DH	A4	2000-091927	3/31/00	Japan			Abstract
DH	A5	2000-078029	3/14/00	Japan			Abstract
DH	A6	11-163742	6/18/99	Japan			Abstract
DH	A7	63-045921	2/26/88	Japan			Abstract
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
DH	A8	P. Robertson et al. "A Comparison of Optimal and Sub-optimal MAP Decoding Algorithms in the Log Domain", Institute for Communications Technology, pgs. 1009-1013, (1995).					
DH	A9	G. Masera et al., "VLSI Architectures for Turbo Codes", IEEE Transactions on VLSI Systems, pgs. 369-379, (1999)					
DH	A10	A.J. Viterbi, "An Intuitive Justification and a Simplified Implementation of the MAP Decoder for Convolutional Codes", IEEE Journal on Selected Areas, pgs. 260-264, (1998).					
DH	A11	S. Benedetto et al., "Soft Output Decoding Algorithms for Continuous Decoding of Parallel Concatenated Convolutional Codes", pgs. 112-117, ICC '96, (1996).					
DH	A12	L.R. Bahl et al., "Optimal decoding of Linear Codes for Minimizing Symbol Error Rate", IEEE Transactions on Information Theory, pgs. 284-287, (1974).					
DH	A13	C. Berou et al., "Near Shannon Limit Error-Correcting Coding and Decoding Turbo-Codes", ICC '95, pgs. 1064-1070, (1993)					
EXAMINER <i>Daniel Llera</i>				DATE CONSIDERED <i>01/03/05</i>			
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